L Number	Hits	Search Text	DB	Time stamp
-	2056	375/232	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/21 15:07
-	92	"5485490"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/21 15:08
-	21	"5757857"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/21 15:09
-	9	"6122336"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/21 15:09
-	13	"6002279"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/21 15:20
-	21	sampling and gigabit and equaliz$3 and quantiz$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/21 15:33
-	11	sampling and multi-gigabit and equaliz$3 and quantiz$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/21 15:33
-	7435	back adj1 plane	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/22 11:14
-	30	router with back adj1 plane	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/22 10:48
-	8	router with back adj1 plane and equalizer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/22 10:55
-	0	back adj1 plane with equalizer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/22 10:56
-	30	router with back adj1 plane	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/22 11:00
-	0	router with back adj1 plane with equalizer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/22 11:00
-	39	router with equalizer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/22 11:18

-	0	router with equalizer with plane	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/22 11:09
-	0	router with equalizer with $5plane	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/22 11:09
-	0	router with equalizer with back-plane	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/22 11:09
-	0	router with equalizer with back adj1 plane	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/22 11:12
-	0	router adj1 plane with equalizer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/22 11:12
-	0	router adj1 plane and equalizer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/22 11:14
-	0	router adj1 plane and equalizer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/22 11:15
-	7435	back adj1 plane	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/23 12:43
-	10	router with plane and equalizer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/22 11:15
-	1	router with (plane and equalizer)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/22 11:16
-	0	router with (back adj1 plane and equalizer)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/22 11:16
-	8	"6404525"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/22 11:49
-	204	(mother adj1 board or PCB) and equalizer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/22 11:50
-	13	(mother adj1 board or PCB) with equalizer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/22 11:50

-	0	(sampl$3 and equaliz$3 and quantiz$3)with analog and gigabot	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/22 12:56
-	11	(sampl$3 and equaliz$3 and quantiz$3)with analog and gigabit	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/22 12:56
-	75	sampler and equalizer and quantizer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/23 08:55
-	8	sampler and equalizer and quantizer and clock adj1 signal$1 and gigabit	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/23 08:58
-	8	sampler and equalizer and quantizer and clock adj1 signal$1 and multi-gigabit	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/23 08:59
-	8	sampler and equalizer and quantizer and clock adj1 signal$1 and gigabit	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/23 09:00
-	22	sampler and equalizer and quantizer and clock adj1 signal	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/23 09:12
-	108	back adj1 plane and interface adj1 board	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/23 12:44
-	11	back adj1 plane and interface adj1 board and equalizer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/23 12:46
-	11	(back adj1 plane or mother adj1 board) and interface adj1 board and equalizer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/23 12:56
-	51	interface adj1 board and equalizer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/23 12:57
-	0	router with interface adj1 board and receiver and equalizer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/23 12:58
-	41	interface adj1 board and receiver and equalizer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/23 13:32
-	12773	backplane interface adj1 board and receiver and equalizer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/23 13:33

-	4	backplane and interface adj1 board and receiver and equalizer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/23 13:53
-	12	(backplane or (back adj1 plane)) and interface adj1 board and receiver and equalizer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/23 14:06
-	119	router and LMS	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/23 14:07
-	4	router with LMS	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/23 14:07
-	34	router with interface and LMS	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/23 14:15
-	0	router with interface adj1 board and LMS	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/23 14:34
-	0	electrical adj1 signal adj1 router with interface adj1 board and LMS	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/23 14:35
-	3	electrical adj1 signal adj1 router	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/23 14:40
-	0	electrical adj1 signal adj1 router with LMS	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/23 14:41
-	5	electrical adj1 signal and interface adj1 board and LMS	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/23 14:49
-	13	"6002279"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/23 14:57
-	138	WDM adj1 coupler and equalizer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/23 14:57
-	73	WDM adj1 coupler and optical adj1 coupler and equalizer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/23 14:57
-	41	WDM adj1 coupler and optical adj1 coupler and controller and equalizer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/23 14:58

-	13	WDM adj1 coupler and optical adj1 coupler and controller and receiver and equalizer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/23 14:58